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L2	10	data converter duty cycle modulator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	WITH	ON	2005/04/04 10:59
L3	81	data converter duty cycle modulator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	ON	2005/04/04 10:59
L4	717	data converter duty cycle	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	ON	2005/04/04 10:59
L5	125	data converter duty cycle	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	WITH	ON	2005/04/04 10:59
L6	387	data duty cycle modulator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	ON	2005/04/04 10:59

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1 A reconfigurable dual output low power digital PWM power converter
Abram Dancy, Anantha Chandrakasan
August 1998 **Proceedings of the 1998 international symposium on Low power electronics and design**
Full text available: pdf(670.26 KB) Additional Information: full citation, abstract, references, citings, index terms

Most work to date on power reduction has focused at the component level, not at the system level. In this paper, we propose a framework for describing the power behavior of system-level designs. The model consists of a set of resources, an environmental workload specification, and a power management policy, which serves as the heart of the system model. We map this model to a simulation-based framework to obtain an estimate of the system's power dissipation. Accompanying th ...

2 Variable supply-voltage scheme with 95%-efficiency DC-DC converter for MPEG-4 codec
Fuyuki Ichiba, Kojiro Suzuki, Shinji Mita, Tadahiro Kuroda, Tohru Furuyama
August 1999 **Proceedings of the 1999 international symposium on Low power electronics and design**
Full text available: pdf(640.57 KB) Additional Information: full citation, references, citings, index terms

Keywords: DC-DC, PWM, low power, low voltage, variable supply voltage

3 A low power switching power supply for self-clocked systems
G. Wei, M. Horowitz
August 1996 **Proceedings of the 1996 international symposium on Low power electronics and design**
Full text available: pdf(612.78 KB) Additional Information: full citation, references, citings, index terms

4 A discrete-time battery model for high-level power estimation
L. Benini, G. Castelli, A. Macii, E. Macii, M. Poncino, R. Scarsi
January 2000 **Proceedings of the conference on Design, automation and test in Europe**
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5 Systems II: Hardware design experiences in ZebraNet
Pei Zhang, Christopher M. Sadler, Stephen A. Lyon, Margaret Martonosi
November 2004 **Proceedings of the 2nd international conference on Embedded networked sensor systems**
Full text available: pdf(472.66 KB) Additional Information: full citation, abstract, references, index terms

The enormous potential for wireless sensor networks to make a positive impact on our society has spawned a great deal of research on the topic, and this research is now producing environment-ready systems. Current technology limits coupled with widely-varying application requirements lead to a diversity of hardware platforms for different portions of the design space. In addition, the unique energy and reliability constraints of a system that must function for months at a time without human i ...

Keywords: GPS, ZebraNet, sensor deployment, sensor networks

6 Design and implementation of a scalable encryption processor with embedded variable DC/DC converter
James Goodman, Anantha Chandrakasan, Abram P. Dancy
June 1999 **Proceedings of the 36th ACM/IEEE conference on Design automation**
Full text available: pdf(119.51 KB) Additional Information: full citation, references, citings, index terms

7 Adaptive control of Bolu highway tunnel ventilation system using fuzzy logic
Ercüment Karakas, Hasan Külünk
February 1998 **Proceedings of the 1998 ACM symposium on Applied Computing**
Full text available: pdf(330.84 KB) Additional Information: full citation, references, index terms

Keywords: adaptive control, fuzzy logic, highway tunnel ventilation

8 Session S8.1: power and battery management: System lifetime extension by battery management: an experimental work
Davide Bruni, Luca Benini, Bruno Riccó
October 2002 **Proceedings of the 2002 international conference on Compilers, architecture, and synthesis for embedded systems**
Full text available: pdf(131.85 KB) Additional Information: full citation, abstract, references, citings, index terms

Many portable devices, like laptops and PDAs can be powered by different combinations of two or more battery packs to give the user the possibility to choose an optimal compromise between lifetime and weight/size. The common discharge policy for multiple battery packs is sequential, i.e., the system switches to the second pack when the first one is empty. In this work we demonstrate that this policy is not optimal by proving the effectiveness of two other policies, namely switched and series, wh ...

Keywords: battery, lifetime extension, power management

9 A technique for testing command and control software
Marvin L. Watkins
April 1982 **Communications of the ACM**, Volume 25 Issue 4

Full text available: pdf(449.51 KB) Additional Information: full citation, abstract, references, citings, index terms

A technique for testing embedded-microprocessor command and control programs is described. The continuity inherent in functions computed by programs which monitor natural phenomena is exploited by a simple difference equation-based algorithm to predict a program's next output from its preceding ones. The predicted output is compared with the actual output while indexing through the program's domain. Outputs which cannot be predicted are flagged as potential errors. Data are presented which ...

Keywords: predicator

10 12-b 125 MSPS CMOS D/A designed for spectral performance
D. Mercer, L. Singer
August 1996 **Proceedings of the 1996 international symposium on Low power electronics and design**

Full text available: pdf(223.48 KB) Additional Information: full citation, references, index terms

11 (Special session) presentation + poster disscussion: university design contest: Fast adaptive DC-DC conversion using dual-loop one-cycle control in standard digital CMOS process
Dongsheng Ma, Wing-Hung Ki, Chi-Ying Tsui
January 2004 **Proceedings of the 2004 conference on Asia South Pacific design automation: electronic design and solution fair 2004**

Full text available: pdf(100.60 KB) Additional Information: full citation, abstract, references

An adaptive switching converter is presented. It adopts a dual-loop one-cycle control for tight line and load regulation, while retaining fast response and good stability. DC level shifting technique eliminates the use of negative supply voltage and enables both continuous and discontinuous conduction operation. Error correction loops greatly tightens output voltage regulation. Dynamic loss control further improves the efficiency for a wide power range. The converter was fabricated with a standa ...

12 Highlights of ISSCC: high-speed heterogenous design techniques: Design of a 10GHz clock distribution network using coupled standing-wave oscillators
Frank O'Mahony, C. Patrick Yue, Mark A. Horowitz, S. Simon Wong
June 2003 **Proceedings of the 40th conference on Design automation**

Full text available: pdf(659.48 KB) Additional Information: full citation, abstract, references, citings, index terms

In this paper, a global clock network that incorporates standing waves and coupled oscillators to distribute a high-frequency clock signal with low skew and low jitter is described. The key design issues involved in generating standing waves on a chip are discussed, including minimizing wire loss within an available technology. A standing-wave oscillator, a distributed oscillator that sustains ideal standing waves on lossy wires, is introduced. A clock grid architecture comprised of coupled, sta ...

Keywords: clock distribution, coupled oscillators, distributed oscillators, on-chip phase measurement, resonant clocking, salphasic, standing wave

13 (Special session) presentation + poster disscussion: university design contest: A dual-band switching digital controller for a buck converter

Martin Yeung-Kei Chui, Wing-Hung Ki, Chi-Ying Tsui

January 2004 **Proceedings of the 2004 conference on Asia South Pacific design automation: electronic design and solution fair 2004**

Full text available: pdf(264.69 KB) Additional Information: full citation, abstract, references

A 0.6μm CMOS integrated digital PID controller for a buck converter is fabricated and tested. It consists of: (1) a VCO driving a counter to serve as an ADC; (2) a PID compensator that employs a programmable integration time for enhancing accuracy and stability; and (3) a dual-band switching PWM generator with a modified tapped delay line for better output resolution and area efficiency. The converter switches at 1 MHz, while the tracking time is 50μs for a step change of IV.

14 RF communication circuits: Integrated DC-DC converter design for improved WCDMA power amplifier efficiency in SiGe BiCMOS technology

Drew Guckenberger, Kevin Kornegay

August 2003 **Proceedings of the 2003 international symposium on Low power electronics and design**

Full text available: pdf(318.15 KB) Additional Information: full citation, abstract, references, index terms

We present a DC-DC converter design for on-chip integration with a WCDMA power amplifier to provide supply voltage modulation and efficiency enhancement. It operates from a 3.3V supply using 0.35μm 'high-breakdown' CMOS transistors available in IBM's SiGe BiCMOS 6HP process. Five selectable output voltage levels are available ranging from 1.3V to 3.3V. The converter is optimized for operation at 88.7MHz. Simulation results show an average efficiency of 78.8% over power amplifier operating c ...

Keywords: CCM-DCM, DC-DC converter, W-switching, WCDMA, efficiency, power amplifier

15 Low power converter circuits: Integrated adaptive DC/DC conversion with adaptive pulse-train technique for low-ripple fast-response regulation

Chuang Zhang, Dongsheng Ma, Ashok Srivastava

August 2004 **Proceedings of the 2004 international symposium on Low power electronics and design**

Full text available: pdf(298.61 KB) Additional Information: full citation, abstract, references, index terms

Dynamic voltage scaling (DVS) is a very effective low-power design technique in modern digital IC systems. On-chip adaptive DC/DC converter, which provides adjustable output voltage, is a key component in implementing DVS-enabled system. This paper presents a new adaptive DC/DC converter design, which adopts a delay-line controller for voltage regulation. With a proposed adaptive pulse-train technique, ripple voltages are reduced by 50%, while the converter still maintains satisfying transient r ...

Keywords: DC/DC conversion, adaptive output, adaptive pulse-train technique, low ripple, transient response

16 Special section on sensor network technology and sensor data managment: The sensor spectrum: technology, trends, and requirements

Joseph M. Hellerstein, Wei Hong, Samuel R. Madden

December 2003 **ACM SIGMOD Record**, Volume 32 Issue 4

Full text available: pdf(1.64 MB) Additional Information: full citation, abstract, references

Though physical sensing instruments have long been used in astronomy, biology, and civil

engineering, the recent emergence of wireless sensor networks and RFID has spurred a renaissance in sensor interest in both academia and industry. In this paper, we examine the spectrum of sensing platforms, from billion dollar satellites to tiny RF tags, and discuss the technological differences between them. We show that battery powered sensor networks, with low-power multihop radios and low-cost processor ...

17 Efficient methods for simulating highly nonlinear multi-rate circuits

Jaijeet Roychowdhury

June 1997 **Proceedings of the 34th annual conference on Design automation - Volume 00**

Full text available: pdf(1.19 MB) Publisher Site Additional Information: full citation, abstract, references, citings, index terms

Widely-separated time scales appear in many electronic circuits, making traditional analysis difficult or impossible if the circuits are highly nonlinear. In this paper, an analyticalformulation and numerical methods are presented for treating strongly nonlinear multi-rate circuits effectively. Multivariate functions in the time domain are used to capturewidely separated rates efficiently, and a special partial differential equation (the MPDE) is shown to relate the multivariate forms of a circu ...

18 All digital built-in delay and crosstalk measurement for on-chip buses

Chauchin Su, Yue-Tsang Chen, Mu-Jeng Huang, Gen-Nan Chen, Chung-Len Lee

January 2000 **Proceedings of the conference on Design, automation and test in Europe**

Full text available: pdf(168.37 KB) Publisher Site Additional Information: full citation, references, index terms

19 Oral presentation session III: energy efficient design: Flexible power scheduling for sensor networks

Barbara Hohlt, Lance Doherty, Eric Brewer

April 2004 **Proceedings of the third international symposium on Information processing in sensor networks**

Full text available: pdf(305.86 KB) Additional Information: full citation, abstract, references, index terms

We propose a distributed on-demand power-management protocol for collecting data in sensor networks. The protocol aims to reduce power consumption while supporting fluctuating demand in the network and provide local routing information and synchronicity without global control. Energy savings are achieved by powering down nodes during idle times identified through dynamic scheduling. We present a real implementation on wireless sensor nodes based on a novel, two-level architecture. We evaluate ou ...

Keywords: communication scheduling, power management, sensor networks

20 Wireless integrated network sensors

G. J. Pottie, W. J. Kaiser

May 2000 **Communications of the ACM**, Volume 43 Issue 5

Full text available: pdf(303.43 KB) html(40.24 KB) Additional Information: full citation, references, citings, index terms

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